



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chakravarthi et al.

Art Unit: 2822

Serial No.: 10/725,977

Examiner: Thomas, T.

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Docket No.: TI-36386

Customer No.: 23494

Conf. No.: 4932

Title: FORMING A RETROGRADE WELL IN A TRANSISTOR TO ENHANCE  
PERFORMANCE OF THE TRANSISTOR

ELECTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

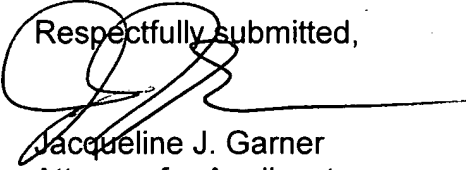
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1-13-05  
Date  
Marianna Smith  
Marianna Smith

Dear Sir:

With respect to the Restriction Requirement mailed on 12/22/2004, the Examiner has restricted the instant application to the invention of Group I (Claims 1-12), or Group II (Claims 13-27). In light of this, Applicants elect to pursue Group I, (Claims 1-12) without traverse.

Respectfully submitted,

  
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